

Claims:

1. (cancelled)

2. (cancelled)

3. (amended) In an amplification circuit using amplifying devices in an
5 amplifier in an audio system, an input and output signal preservation circuit of
an amplification circuit, comprising:

an input signal preservation unit that is connected with an input terminal
of an amplification device of an amplification circuit capable of amplifying an
AC input signal using a certain amplification device and outputting the same
10 and increases an input resistance value with respect to the input signal and
prevents an attenuation of the input signal, said input signal preservation unit
including a bias resistor connected in parallel with an input terminal of the
amplification device; and a reactor that is connected in series with the bias
resistor and operates as an AC resistor and increases an input resistance
15 value together with the bias resistor for thereby preventing a leakage of the AC
input signal, and

an output signal preservation unit that is connived with an output
terminal of the amplification device and increases an output resistance value
with respect to the amplification signal and prevents an attenuation of the
20 amplification output signal for thereby compensating a voltage variation ratio

with respect to the amplification output signal, said output signal preservation unit including, an amplification signal preservation part that includes a load resistor connected in parallel with an output terminal of the amplification device, and a first reactor that is connected in series with the load resistor and operates as an AC resistor and increases an output resistance value together with the load resistor for thereby preventing a leakage of the amplification output signal; and a voltage compensation unit that includes a second reactor connected in parallel with a connection point of the load resistor and the first reactor and operating as an AC resistor for thereby increasing an output resistance value together with the load resistor and preventing a leakage of the amplification output signal, and a condenser that is connected in series with the second reactor and compensates the voltage variation ratio of the amplification output signal.

AMENDED SHEET (ARTICLE 19)